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New High Performance SOSA Aligned ATR Enclosures From Pixus Save Space

Waterloo, Ontario — Apr 18, 2025 – Pixus Technologies, a provider of embedded computing and enclosure solutions, has developed new SOSA aligned ATR enclosures utilizing the 3U OpenVPX form factor. The various chassis platforms typically support 100GbE or higher speeds.

The new ARINC 404 5/8 size ATRs from Pixus features customized I/O options and various SOSA slot profile options, including RF and optical interfaces through the backplane. For chassis management, the ATR has the option of implementing Pixus' SOSA aligned Tier 3 mezzanine-based solution that sits behind the backplane. This saves a slot of space while acting as a health monitor and control module for the system.

Pixus also offers an Ethernet Converter board that fits behind the I/O board for converting 10GBASE-KR to 10GBASE-T, again saving a slot of space. Various configurations of the chassis include a "top hat" or "bottom tub" design for extra space above or below the plug in cards for other devices, etc.

Pixus provides OpenVPX backplane/chassis systems in commercial, development, and MIL rugged formats. The company also offers IEEE and Eurocard components for the embedded computer market.

About Pixus Technologies

Leveraging over 20 years of innovative standard products, the Pixus team is comprised of industry experts in electronics packaging. Founded in 2009 by senior management from Kaparel Corporation, Pixus Technologies' embedded backplanes and systems are focused primarily on SOSA, OpenVPX, xTCA, VME, cPCI Serial, and custom designs. Pixus USA was formed in 2022, based in Tonawanda, NY.