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## **New CMOSS & SOSA Aligned Development Backplane From Pixus**

Waterloo, Ontario — February 17, 2025 – Pixus Technologies, a provider of embedded computing and enclosure solutions, has announced a new C5ISR Modular Suite of Standards (CMOSS) and SOSA development backplane that supports various I/O intensive, compute intensive, switches, PNT slots, and VITA 62 PSU slots.

The 8-slot development backplane only has the utility plane routed (including power, ground, SMBus, clock, and utility signals). This allows a versatile range of plug-in card configurations to be utilized. Three of the slots have a cutouts for VITA 67.3c interfaces and one slot has a VITA 67.3d cutout. The unit can be purchased in an open frame, rackmount/desktop, or other chassis format along with a Pixus SOSA aligned Tier 3 chassis manager development kit, which cables to the backplane.

Pixus offers various OpenVPX backplane/chassis systems in commercial, development, and MIL rugged formats. The company also provides IEEE and Eurocard components for the embedded computer market.

### **About Pixus Technologies**

Leveraging over 20 years of innovative standard products, the Pixus team is comprised of industry experts in electronics packaging. Founded in 2009 by senior management from Kaparel Corporation, a Rittal company, Pixus Technologies' embedded backplanes and systems are focused primarily on ATCA, OpenVPX, MicroTCA, and custom designs. Pixus also has an extensive offering of VME-based and cPCI-based solutions. In May 2011, Pixus Technologies became the sole authorized North and South American supplier of the electronic packaging products previously offered by Kaparel Corporation and Rittal.