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## **New 10-slot 3U Development Chassis for OpenVPX, SOSA Designs**

Waterloo, Ontario — Apr 01, 2021 – Pixus Technologies, a provider of embedded computing and enclosure solutions, has announced a new 10-slot 3U OpenVPX enclosure system geared for SOSA (Sensor Open Standard Architecture) requirements.

The new chassis platform features removable sidewalls, allowing the open frame enclosure to be optional fully enclosed. The 10-slot backplane has clocking and VITA 46.11 system management connections per SOSA requirements. It has multiple VITA 67.3c and VITA 67.3d cutouts for RF interfaces through the backplane for 3U OpenVPX boards. The speed of the backplane supports 40GbE and PCIe Gen3 signals with high-grade PCB material and back-drilling. Pixus can also support backplane design speeds to 100GbE and beyond. Other design options of the development chassis include a switch to go from Pixus' modular fixed PSU to a VITA 62 power supply and card guides for conduction-cooled boards.

Pixus' vast array of OpenVPX backplane and chassis platforms designed for SOSA implementations allows customers to find a standard configuration with short lead-times and reduced costs. The company offers OpenVPX backplane/chassis systems in commercial, development, and MIL rugged formats. The company also provides IEEE and Eurocard components for the embedded computer market.

### **About Pixus Technologies**

Leveraging over 20 years of innovative standard products, the Pixus team is comprised of industry experts in electronics packaging. Founded in 2009 by senior management from Kaparel Corporation, a Rittal company, Pixus Technologies' embedded backplanes and systems are focused primarily on ATCA, OpenVPX, MicroTCA, and custom designs. Pixus also has an extensive offering of VME-based and cPCI-based solutions. In May 2011, Pixus Technologies became the sole authorized North and South American supplier of the electronic packaging products previously offered by Kaparel Corporation and Rittal.